

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-6 (cancelled)

7. (Previously Presented) The sensor of claim 38, wherein the second repeat control circuit operates each time a point in a moving image projected on the column of pixels transverses a pixel boundary.

8. (Previously Presented) The sensor of claim 7, wherein the second repeat control circuit repetitively operates until the point in the moving image traverses the column of pixels.

Claims 9-15 (cancelled)

16. (Previously Presented) The sensor of claim 38 further comprising:
an output bus; and
a third plurality of switches, each switch being coupled between the output bus and a corresponding accumulator of the column of accumulators.

17. (Previously Presented) The sensor of claim 16 further comprising a third switch control circuit operable to control a switch of the third plurality of switches to connect the accumulated signal from the accumulator of the column of accumulators to the output bus while controlling all remaining switches of the third plurality of switches to isolate the output bus from all remaining accumulators of the column of accumulators.

18. (Previously Presented) The sensor of claim 17 further comprising a third increment control circuit operable to increment the third switch control circuit to control a next in succession switch of the third plurality of switches to connect the accumulated signal at a next in succession accumulator of the column of accumulators to the output bus while controlling all

remaining switches of the third plurality of switches to isolate the output bus from all remaining accumulators of the column of accumulators.

19. (Original) The sensor of claim 18, wherein:
the second repeat control circuit repetitively operates the third increment control circuit and the third switch control circuit; and
each repetitive operation of the third switch control circuit couples a signal from a successive accumulator of the column of accumulators.

20. (Original) The sensor of claim 19, wherein each time the second repeat control circuit is operated, the second repeat control circuit repetitively operates the third increment control circuit and the third switch control circuit until all accumulators of the column of accumulators have been successively coupled onto the output bus.

21. (Previously Presented) The sensor of claim 20, wherein the second repeat control circuit operates each time a point in a moving image projected on the column of pixels transverses a pixel boundary.

22. (Previously Presented) The sensor of claim 21, wherein the second repeat control circuit repetitively operates until the point in the moving image traverses the column of pixels.

Claims 23-37 (cancelled)

38. (Previously Presented) A sensor comprising:
a column bus;
a column of pixels;
a first plurality of switches, each switch being coupled between the column bus and a corresponding pixel of the column of pixels;
a column of accumulators;

a second plurality of switches, each switch being coupled between the column bus and a corresponding accumulator of the column of accumulators;

a first switch control circuit operable to control a switch of the first plurality of switches to connect the signal from the pixel of the column of pixels to the column bus while controlling all remaining switches of the first plurality of switches to isolate the column bus from all remaining pixels of the column of pixels;

a second switch control circuit operable to control a switch of the second plurality of switches to connect the signal on the column bus to the accumulator of the column of accumulators while controlling all remaining switches of the second plurality of switches to isolate the column bus from all remaining accumulators of the column of accumulators;

a first increment control circuit operable to increment the first switch control circuit to control a next in succession switch of the first plurality of switches to connect a signal from a next in succession pixel of the column of pixels to the column bus while controlling all remaining switches of the first plurality of switches to isolate the column bus from all remaining pixels of the column of pixels;

a second increment control circuit operable to increment the second switch control circuit to control a next in succession switch of the second plurality of switches to connect the signal on the column bus to a next in succession accumulator of the column of accumulators while controlling all remaining switches of the second plurality of switches to isolate the column bus from all remaining accumulators of the column of accumulators;

a first repeat control circuit operable to repetitively operate the first increment control circuit, the first switch control circuit, the second increment control circuit, and the second switch control circuit; and

a second repeat control circuit operable to operate the first increment control circuit and then operate the first repeat control circuit each time the second repeat control circuit is operated.